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PWM Rectifier Control Comparison for Aerospace Applications

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Abstract

High-voltage DC-bus power quality is examined when the power network is supplied by a PWM rectifier controlled using different DC-bus voltage regulation schemes. Three controllers are compared, a voltage oriented control and two model predictive control schemes, predictive current fixed frequency and predictive direct power control. A set of PWM rectifier simulation models illustrate the performance of the controllers; a switched model, an ideal averaged-value model and an enhanced averaged-value model which contains the key loss mechanisms. The DC-bus transients are within the limits defined in MIL-STD-704F only when a predictive direct power control is employed.

1 Introduction

Advanced more-electric-aircrafts (MEAs) are anticipated to have multiple gas-engine driven variable frequency generators [1] and high levels of diverse, highly-dynamic, and often safety critical, power electronics based load systems interconnected through a single power network. DC power network architectures are a contender for future MEA [1] as the power conversion stages and the paralleling of multiple power sources is less complex than an equivalent AC network, however, challenges still exist, including protection and network power quality. A candidate power network architecture for future aircraft is higher voltage DC, either a single 270 V channel or ±270 V.

Strict limits are defined in the MIL-Std704F [2] on the allowable steady-state and transient DC network voltage. To achieve the DC-bus power quality defined in MIL-Std-704F, one solution is to use a power electronic interface between the gas-engine embedded variable speed generator and the network. For fixed nominal voltage DC networks a PWM boost rectifier [3] is a candidate interface converter.

A Saber simulation model of a 10 kW 270 V DC three-phase PWM rectifier is used to evaluate the different models/controllers. In Fig. 1 three rectifier models can be used; a switched model, and either an ideal [4] or enhanced averaged-value models. The enhanced averaged-value PWM rectifier model includes the key circuit loss mechanisms, including device conduction and switching, and non-linear effects (dead-time and over-modulation) which may affect the circuit efficiency.

Figure 1. System configuration.

Multiple DC voltage controllers have been applied to Fig. 1; vector oriented control (VOC), predictive current fixed frequency (PCFF) [5] and predictive direct power control (PDPC) [6]. The three DC voltage control strategies are examined with a focus on unity AC power factor, DC power quality (voltage deviation) and circuit efficiency for a range of typical aircraft load systems.

The system in Fig. 1 is part of a larger suite of models which have been developed in an EU JTI Clean Sky project to enable investigations to be undertaken into the behaviour and energy efficiency of alternative electric drive solutions and technologies over a range of aircraft electrical system architectures and operating scenarios.

In the following sections the ideal and enhanced averaged-value model equations are briefly discussed and simulation results are presented to examine the performance of the models. The design of the different DC-bus voltage regulators is then presented and simulation results are used to illustrate the performance of the controllers.

2 PWM Rectifier Model

The structure of the six-switch three-phase PWM boost rectifier which is used to energise the aircraft DC network is shown in Fig. 2.

Figure 2. Three-phase PWM rectifier switched model.

Averaged-value models of the PWM rectifier are developed to allow the converter performance over long duration load
profiles to be evaluated with an acceptable computation time.

2.1 Ideal averaged-value

The ideal averaged-value model [4] synthesises the mid-point voltage of each rectifier leg, \( v_a, v_b, v_c \), shown in Fig. 2, to produce an equivalent DC voltage which is imposed across the output capacitor. The DC side is a current sink, due to the voltage imposed across the output impedance (capacitor and load), and so the rectifier is a current source from the AC side.

Equation (1) can be used to define the per-phase duty cycle, \( d_z \), where 'z' in (1) can be either a, b or c.

\[
d_z = \frac{1 + m_z}{2} \sin(\omega t - \delta - x)
\]

(1)

\( m_z \) is the depth of modulation, \( \delta \) is the phase shift between the AC supply and the input to the rectifier half-bridge, \( \omega \) is the supply frequency and ‘x’ is the 120° phase shift between each phase. Assuming the use of bipolar naturally sampled PWM, avoid over-modulation, and current, \( i_{xz} \), can be expressed as functions of the respective duty cycle, so:

\[
v_z = \frac{(2d_z - 1)}{2} v_{DC} = \frac{m_z v_{DC}}{2} \sin(\omega t - \delta - x)
\]

(2)

The AC current per-phase can be written as:

\[
i_{xz} = i_{xz} \sin(\omega t - \phi - x)
\]

(3)

where \( \phi \) is the angle between the per-phase AC voltage, \( v_z \), and current, \( i_{xz} \).

If the three-phase PWM rectifier is assumed lossless, then power input to the bridge is equal to the power delivered to the output side. Eq. (2) can then be substituted in the power balance equation to give an expression for the DC output current, (4).

\[
i_0 = \sum \left( \left( d_z - \frac{1}{2} \right) i_{xz} \right)
\]

(4)

2.2 Enhanced averaged-value

The ideal averaged-value rectifier model is enhanced to include the dominant features of a practical PWM rectifier which may affect the system level performance of the power converter. The loss mechanisms which are considered are IGBT and diode conduction voltage drops, switching losses and dead-time. The circuit losses are defined using simple expressions based on parameters readily available in component datasheets.

Conduction losses and dead-time

The conduction losses are calculated by approximating the device voltage drop as a threshold voltage and a voltage across the internal resistance [7]. Therefore, the voltage drop across the IGBT and diode can be expressed by (5) and (6) respectively. The current component in (5) and (6) is taken as an absolute value as unidirectional IGBT’s are considered.

\[
v_{IGBT} = v_{th-IGBT} + |i_{xz}| R_{IGBT}
\]

(5)

\[
v_{diode} = v_{th-diode} + |i_{xz}| R_{diode}
\]

(6)

where \( v_{th-IGBT} \) and \( v_{th-diode} \) are the IGBT and diode threshold voltages and \( R_{IGBT} \) and \( R_{diode} \) are the IGBT and diode internal resistances.

Dead-time is an essential feature of most practical power converters to prevent damage to the power devices. When dead-time is incorporated in the averaged-value model equations the polarity of the AC current determines how dead-time affects the phase voltage and DC output current.

Equations (5) and (6), together with the effect of dead-time, \( D \), both need to be integrated with (2) by summing the volt-seconds for the averaged phase voltage over a switching period, \( T_{sw} \). The expression for the phase voltage, \( v_z \), during positive AC current is given by (7), and (8) for negative AC current.

\[
\begin{align*}
v_z &= \left[ 2(d_z + D_{DT}) - 1 \right] \frac{v_{DC}}{2} + (d_z + D_{DT}) v_{diode} + \left[ 1 - (d_z + D_{DT}) \right] v_{IGBT} \\
v_z &= \left[ 2(d_z - D_{DT}) - 1 \right] \frac{v_{DC}}{2} - \left[ 1 - (d_z - D_{DT}) \right] v_{diode} - (d_z - D_{DT}) v_{IGBT}
\end{align*}
\]

(7)

(8)

where \( D_{DT} \) is the ratio of dead-time, \( D \), to switching period, \( T_{sw} \); \( D_{DT} = \frac{D}{T_{sw}} \).

Dead-time also affects the DC current contribution per-phase, to account for this (4) must be modified by considering the device conduction. Equations (9) and (10), for positive and negative AC current respectively, are the per-phase output current contributions including dead-time which must be summed to form the total DC output current.

\[
\begin{align*}
i_0 &= \left( d_z + D_{DT} - \frac{1}{2} \right) i_{xz} \\
i_0 &= \left( d_z - D_{DT} - \frac{1}{2} \right) i_{xz}
\end{align*}
\]

(9)

(10)

Switching losses

Switching losses are produced when hard switching of the power devices is used. The switching losses are a function of device turn-on, \( E_{ON} \), and turn-off energy, \( E_{OFF} \), and switching frequency, \( f_{sw} \) [7]. Datasets often quote a figure for turn-on and turn-off energy for a single test point, \( V_{DC-Nom}, I_{Kom} \), which sums the IGBT and diodes losses so a simple linear scaling is used to map the losses to other operating points, \( V_{DC}, i_0 \). The scaled switching losses per-phase are given by:

\[
P_{sw} = \sum \left( E_{ON} \frac{v_{DC}}{V_{DC-Nom}} i_{Kom} + E_{OFF} \frac{v_{DC}}{V_{DC-Nom}} i_{Kom} \right) f_{sw}
\]

(11)

The switching power loss can be calculated for all phases and summed to give the total power loss. This power can be converted to a DC current component, by dividing by the DC voltage, and the current component can then be combined with the total DC output current.

2.3 Simulation result comparison

The three PWM rectifier models, a switched, and ideal and enhanced (lossy) averaged-value, have been implemented in
SABER software. A 10 kW 270 V rectifier is simulated and the AC and DC circuit parameters are listed in Table 1. The AC circuit is assumed balanced so all per-phase impedances are the same and the generator saliency is neglected. The rectifier has a relatively small value of DC-bus capacitance due to the requirement to avoid electrolytic capacitor types in aerospace systems. Two load conditions are examined, 14.58 Ω (5 kW) and 7.29 Ω (10 kW).

<table>
<thead>
<tr>
<th>Parameter description</th>
<th>Symbol</th>
<th>Value</th>
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</thead>
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<tr>
<td>Back emf magnitude</td>
<td>$E$</td>
<td>0.0557 Vrads$^{-1}$</td>
</tr>
<tr>
<td>Nominal frequency</td>
<td>$f$</td>
<td>200 Hz</td>
</tr>
<tr>
<td>Line inductance</td>
<td>$L_x$</td>
<td>0.4 mH</td>
</tr>
<tr>
<td>Line resistance</td>
<td>$R_x$</td>
<td>0.15 mΩ</td>
</tr>
<tr>
<td>Output capacitance</td>
<td>$C_{DC}$</td>
<td>800 μF</td>
</tr>
</tbody>
</table>

Table 1. Test electrical system specification.

The devices used in the enhanced (lossy) averaged-value model are the 600 V 100 A Dawin IGBT module [8]; the parameters are listed in Table A.1 in Appendix A. The dead-time, $D$, is 2μs and the switching frequency, $f_s$, is 10 kHz.

Each PWM rectifier model is commanded by a different duty-cycle, defined in the figure, which gives 270 V in the ideal averaged-value model. Figs. 3 and 4 show sample results from all three PWM rectifier models. The top plots show DC voltage and the bottom plots back emf and AC current for phase a only; the other AC currents are omitted for clarity.

Figure 3. Key waveforms for uncontrolled switched, ideal and enhanced (lossy) averaged-value models for a 14.58 Ω load (at 200 Hz $m_z=0.546$ and $\delta=19.04^\circ$, at 300 Hz $m_z=0.794$ and $\delta=12.95^\circ$).

The DC voltage, Figs. 3.a and 4.a, from all models correlate well for the test conditions examined. In all cases the DC voltage from the enhanced (lossy) model is slightly less than the ideal model due to the effect of the losses which requires adjustment to the duty-cycle to obtain the correct DC voltage. The difference in DC voltage from the enhanced and ideal model increases with load which is consistent with the efficiency being slightly lower in the higher load results. Figs. 3.b and 4.b shows AC current for a short time period to illustrate the detail of the three models. In the results shown all PWM rectifier models are operating at unity AC supply power factor. The lossy averaged-value PWM rectifier model efficiency with the 14.58 Ω load is 96.1 % for 200 Hz operation and 97.2 % for 300 Hz operation.

Figure 4. Key waveforms for uncontrolled switched, ideal and enhanced (lossy) averaged-value models for a 7.29 Ω load (at 200 Hz $m_z=0.626$ and $\delta=34.89^\circ$, at 300 Hz $m_z=0.849$ and $\delta=24.89^\circ$).

For the 7.29 Ω load condition in Fig. 4 the lossy averaged-value PWM rectifier model efficiency is 95.7 % for 200 Hz operation and 96.8 % for 300 Hz operation.

3 Control Design

This section presents an overview of three control techniques which enables the DC voltage to be regulated whilst also enabling the AC supply power factor to be controlled. Operation at unity AC side power factor is preferred, where the rectifier is controlled so the AC current, $i_{AC}$, is in phase with the back emf, $E_x$. The modulation limit of the converter may cause the rectifier to operate in a field weakening mode at high frequency; only unity AC power factor operation is considered in this section.

3.1 Voltage oriented control

An overview of the voltage oriented control (VOC) DC voltage regulator is shown in Fig. 5. The VOC uses the well-known amplitude invariant Clarke (between the three phase system and the stationary coordinates, $aβ$) and the Park transform (from $aβ$ synchronous rotating coordinates, $dq$) to translate the three-phase voltage and current into $dq$ equivalents.
The AC power of the rectifier, for the amplitude invariant Clarke transform with orientation of the control to the back emf, is:

$$P_{ac} = \frac{3}{2}(E_d I_{sd} + E_{sq} I_{sq}) = \frac{3}{2}(\frac{3}{2} |E| I_{sd})$$  \quad (17)

Then assuming a lossless converter (only when tuning the DC voltage loop) and so $P_{ac} = P_{dc}$, then as the back emf is slowly varying with time, a transfer function can be developed for the system:

$$G_a(s) = \frac{V_{dc}^2}{I_{sd}} = \frac{3|E|}{s c_{ac} + \frac{1}{R_{load}}}$$ \quad (18)

The transfer function (18) involving $V_{dc}^2$ is not linear, however, we can make the transfer function linear by choosing a new state variable, $U$, which equals $V_{dc}^2$, so:

$$G_a(s) = \frac{U}{I_{sd}} = \frac{3|E|}{s c_{dc} + \frac{1}{R_{load}}}$$  \quad (19)

Eq. (19) contains two parameters which may vary during the simulation time, back emf and the load resistor. Bode plots have been used to determine that the low load point has the highest gain at low frequency which is likely to be within the bandwidth of the controller; this operating point is used to design the $V_{dc}^2$ control.

A simple method is used to tune the PI gains, similar to that used to design the current regulation, where the system is compared to the classic second order differential equation as this yields separate equations for the PI controller gains $k_{p-v}$ and $k_{i-v}$, (20) and (21) respectively.

$$k_{p-v} = \left(2\zeta \omega_n - \frac{2}{c_{dc} R_{load}} \right) \frac{c_{dc}}{3|E|}$$ \quad (20)

$$k_{i-v} = \frac{c_{dc} \omega_n^2}{3|E|}$$ \quad (21)

The tuning of the PI controller is demonstrated for the system detailed in Table 1. The damping factor, $\zeta$, is set to 0.7 and the natural frequency, $f_n$, is 20 Hz. From (20), $k_{i-v} = 0.06$ and from (21), $k_{p-v} = 0.651 \text{m for a } 500 \Omega \text{ load.}$ The cross-over frequency for a 7.29 $\Omega$ load is 8.33 Hz with a phase margin of 111°, and for a 500 $\Omega$ load the cross-over frequency is 30.24 Hz with a phase margin of 65.58°.

3.2 Predictive control fixed switching frequency

Predictive current fixed frequency (PCFF) [5] control is designed to enable the phase current to change from its present value to the desired value in a single switching cycle. The new current reference is $i_{sd}$, the present value of phase current is $i_{sq}$ and so the required rate-of-change of phase current to achieve the new reference value in one switching cycle, $T_{sw}$, is:

$$\frac{di_{sd}}{dt} = \frac{i_{sd} - i_{sq}}{T_{sw}}$$ \quad (22)

Equation (22) can be used in the equation for the AC circuit, from Fig. 2, to determine the converter mid-point voltage, $V_{2}$. The converter mid-point voltage can then be scaled for use in
the PWM modulator, or applied directly to the averaged-value models.

The required AC current is often sinusoidal and to enable unity AC power factor the AC current should be in phase with the back emf. The magnitude of the AC current is determined by a DC voltage regulator with a feed-forward load term. The PI values in the DC voltage controller are set for maximum disturbance rejection where the DC voltage response for the maximum load change was examined for a range of different controller values. The final controller values are $k_p = 0.65$ and $k_i = 40$, which for a 9.5 kW load step from 0.5 kW to 10 kW results in a -42.2 V deviation in DC voltage which is within the -70 V limit of MIL-Std704F.

### 3.3 Predictive direct power control

The objective of a predictive direct power control (PDPC) [6] is to force the instantaneous active and reactive power in the rectifier circuit to equal the reference values at each switching period, $T_{sw}$. The methodology for determining the PDPC algorithm is given in [6] and is not repeated here. The rectifier input voltage vector during each switching cycle, assuming the fundamental frequency is much less than the switching frequency, is determined using a linear extrapolation between the present and predicted future real and reactive power reference values. The real power reference value is determined by a DC voltage controller and the reactive power reference value is used to set the AC supply power factor and so $Q^*_{AC}$ is zero.

As in the PCFF scheme, a PI control is used in the DC voltage controller. Again the maximum load condition was used to set the PI tuning values. The final control values were set at $k_p = 0.4$ and $k_i = 50$, which for a 9.5 kW load step from 0.5 kW to 10 kW causes a -43.2 V deviation in DC voltage which is well within the MIL-Std704F limit.

### 3.4 Controller comparison

Fig. 6 compares the performance of the VOC, PFCC and PDPC for the circuit parameters listed in Table 1 and the device parameters in Table A.1. Only the enhanced (lossy) averaged-value model is used in this comparison to enable the circuit efficiency to be evaluated and to also enable the controller design, which assumes a lossless, converter to be evaluated. The DC voltage reference is initially 240 V and is stepped to 270 V at 0.1 s. The load resistor is initially 145.9 Ω (500 W), then at 0.2 s it is stepped to 14.58 Ω (5 kW), at 0.5 s it returns to 145.9 Ω and it is stepped to 7.29 Ω (10 kW) at 0.8 s. To demonstrate the regenerative capability of the model a 48 A (13 kW) constant current is injected into the DC circuit at 1.1 s. All controllers are regulating the DC bus voltage while ensuring unity AC supply power factor.

The PDPC is the only method tested that meets the MIL-Std704F for allowed DC voltage deviation; the VOC exceeds the upper 330 V limit on the load decrease steps and the lower 200 V limit during the large load increase step as shown in Fig. 6.a, whereas the PCFF exceeds the standards when the power flow reverses at 1.1 s. The PCFF performs well for the load changes, offering only slightly higher voltage deviations with a longer settling time when compared to the PDPC. Fig. 6.b shows AC current for two short time periods to illustrate the detail of the different controller responses; on the scale in Fig. 6.b no difference is noticeable between the AC currents which are all overlaid. The left hand plot in Fig. 6.b shows AC current during the 7.29 Ω load showing unity AC power factor operation, and the right hand plot is for the regenerative period showing power flow to the AC supply. When the circuit is controlled using the PDPC algorithm the circuit efficiency is 98.4 % on the highest load of 7.29 Ω (10 kW), and 98.2 % during regeneration.

![Figure 6](image_url)

Figure 6. Key waveforms for VOC, PCFF and PDPC controlled enhanced (lossy) averaged-value models at 200 Hz.

Fig. 7 shows simulation results for the same controllers, test conditions and circuit/device parameters as used in Fig. 6. The back emf fundamental frequency is increased to 300 Hz and the back emf magnitude is 105 V. Again only the enhanced (lossy) averaged-value model is used in this comparison to enable the efficiency to be evaluated. All controllers are regulating the DC bus voltage while ensuring unity AC supply power factor.

Both the PDPC and PFCC meet the MIL-Std704F allowable voltage deviation limits for the load step events. At 300 Hz fundamental frequency the PFCC results in smaller voltage deviations in Fig. 7.a and a shorter settling time than the PDPC, which is opposite to the 200 Hz results in Fig. 6.a. The DC voltage transient shown in Fig. 7.a at 1.1 s when the PFCC method is used exceeds the MIL-Std-704F limit, whereas the DC voltage transient from the PDPC technique is within the limit. The VOC DC voltage exceeds the standards on the large power increase step and the power reversal. All controllers enable unity AC power factor performance as shown in Fig. 7.b; on the scale in Fig. 7.b no difference is noticeable between the AC currents which are all overlaid. The circuit efficiency for the PDPC method is 98.62% on the highest load of 7.29 Ω (10 kW), and 98.8 % during regeneration.
4 Conclusions

An ideal averaged-value model for the three-phase PWM rectifier was enhanced to include IGBT and diode conduction voltage drops, switching losses, dead-time and over-modulation, to enable the efficiency of the PWM rectifier to be determined for power management studies. The averaged-value models were validated by a fully-switched model for different load points and AC circuit parameters. Three different DC voltage controllers, VOC, PCFF and PDPC, have been investigated with both model predictive controllers, the PFCC and the PDPC, resulting in DC voltage transients well within the MIL-Std-704F limits for the load transients examined. Only the PDPC method resulted in acceptable DC voltage transients when the power flow reversed to demonstrate regeneration. The use of the enhanced (lossy) averaged-value model enables the circuit efficiency to be evaluated over the different load conditions.

More generally the work in this paper illustrates the importance of considering the PWM rectifier efficiency when designing on-board power networks to ensure the appropriate system and component sizing. The PWM rectifier control has a significant influence on the system quality and both the architecture and tuning must be carefully designed for the specific operation range of the system. Further work on the control includes parameter sensitivity of all methods considered.

Acknowledgements

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References


Appendix A

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<tr>
<th>Parameter description</th>
<th>Symbol</th>
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<td>$V_{th\text{-diode}}$</td>
<td>1.35 V</td>
</tr>
<tr>
<td>Diode resistor</td>
<td>$R_{diode}$</td>
<td>6.7 mΩ</td>
</tr>
<tr>
<td>IGBT threshold voltage</td>
<td>$V_{th\text{-igbt}}$</td>
<td>1.2 V</td>
</tr>
<tr>
<td>IGBT resistor</td>
<td>$R_{igbt}$</td>
<td>1.25 mΩ</td>
</tr>
<tr>
<td>Switching frequency</td>
<td>$f_{sw}$</td>
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</tr>
<tr>
<td>Dead time</td>
<td>$D$</td>
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<td>Saturation limit for PWM</td>
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Table A.1. Enhanced PWM rectifier parameter values for Dawin 600 V 100 A IGBT [8].