Enhanced stability of poly(3-hexylthiophene) transistors with optimally cured poly(methyl methacrylate) dielectric layers

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\textbf{A B S T R A C T}

Poly(3-hexylthiophene)-based organic field-effect transistors (OFETs) have been fabricated on poly(methyl methacrylate) (PMMA) gate dielectric layers under different process conditions, resulting in very different device stability in ambient air. The dielectric layers were prepared by spin coating and subsequently curing at various temperatures (120, 150, and 180 °C) or by ultraviolet light (UV) exposure. With respect to the variations of the on/off current ratio and the threshold voltage, dramatically enhanced stability of the OFETs with the PMMA layer cured at 150 °C has been demonstrated when compared to those cured at different temperatures. The devices cured by UV exposure showed even more superior stability, with reliable performance in ambient air for more than 10 days. The differences in the film surface morphology were analyzed and possible mechanisms for the enhanced stability are discussed.

\section{1. Introduction}

Soluble conjugated polymers are very attractive for use in plastic electronics because they can be deposited and processed at low temperature over a large substrate area. Among various types of polymer semiconductors, regioregular poly(3-hexylthiophene)-2,5-diyl (P3HT) is the most explored due to its wide commercial availability, high carrier mobility and reasonable on/off current ratios [1,2]. However, it is still very challenging to fabricate and measure P3HT-based organic field-effect transistors (OFETs) in ambient conditions due to the rapid undesirable interactions of P3HT with atmospheric oxygen, moisture, ultraviolet light (UV) as well as other species in ambient air [3–5].

Efforts have been attempted to improve the device performance and reliability in air. McCulloch et al. explored strategies to chemically modify the thiophene backbone structure, which led to changes in both backbone conformation and microstructures as well as the electronic energy levels of the molecular orbitals [6]. The substitution of the central thiophene unit with naphthalene resulted in an increase in ionization potential (IP) to improve the air stability but reduce the charge carrier mobility. Recently, his group reported on new semiconducting thiophene[3,2-b]thiophene polymers with the enhancement in charge-carrier mobility and good transistor stability under static storage and operation in a low-humidity air environment due to the unusual liquid–crystalline morphological properties [7]. Also, Ong et al. reported a class of air-stable polythiophenes termed poly(3,3′-dialkyl-quaterthiophene)s through structural design for proper self-assembly ability and controlled π-conjugation [8].

Besides these chemical structural designs, it was reported that OFET performance depended significantly on the way the films were prepared, the kind of dielectric gate insulator used, pretreatment of the gate insulator, and the solvents used for film deposition [9–15]. Our previous work investigated in the influence of process conditions on the stability of P3HT-based OFETs [16]. An improvement in ambient-air stability, that a 120 min exposure to air caused a slight degradation of OFETs, was achieved via a combination of a high boiling-point solvent, a highly hydrophobic self-assembled monolayer (SAM) for dielectric surface treatment, and thermal annealing in a nitrogen gas. The experiments demonstrated that the stability of OFETs may be improved not only by optimization of the chemical structures of the organic semiconductor, but also by refining the device fabrication processes. The latter approach is quite convenient because it offers to the final users the possibility of achieving a stable OFET with the same materials only by changing the processing conditions.

In this work, we fabricated bottom-gate top-contact P3HT-based OFETs embedding a PMMA layer between the silicon dioxide dielectric and the active P3HT channel. The influence of the PMMA layers under different process conditions on the device ambient-air stability has been investigated. We demonstrated that P3HT
devices on optimally cured PMMA layers showed improved stability, with high on/off current ratio and relatively stable threshold voltage, when exposed to ambient conditions for more than 10 days. To make use of the full advantages of organic electronic devices, namely, large area coverage, mechanical flexibility, and low-cost processing, polymer gate dielectrics are the most suitable candidates in OFETs. Recently, Bao’s group has demonstrated the thin poly(4-vinylphenol) (PVP) cross-linked polymer dielectric films for low-voltage organic transistors without the help of any additional inorganic insulating layers [17]. Besides PVP, PMMA is among the most promising polymeric materials due to the easy processing, high stability, and outstanding chemico-physical properties [18,19]. Our work about the PMMA layers cured under different process conditions here not only provides information on the effects of dielectric treatment on device properties, but also leads to important implications in realization and optimization of the performance of organic electronic devices.

2. Experiments

For convenience and a direct comparison with most previous studies, highly doped silicon wafers with a thermally grown SiO2 (300 nm) were used as substrates. Bottom-gate top-contact OFETs based on regioregular P3HT (Rieke metals, used as received) were fabricated on the PMMA buffer layer on the substrates. PMMA films were prepared from solution of PMMA (Sigma–Aldrich, molecular weight 350K) and n-butyl acetate, by spin-coating and subsequent curing at various temperatures or by UV exposure. For the thermal treatment, PMMA layers were cured at different temperatures (120, 150, and 180 °C) for one hour in a slightly overpressured nitrogen gas. The UV exposure was performed at the UV wavelength of 365 nm and the density of 18.3 mW/cm² for 1 min. For all samples, P3HT was dissolved in 1,2,4-trichlorobenzene (TCB) at a concentration of 10 mg/ml and was spin-coated at 2000 rpm on top of the cured PMMA layers in air. After that, the samples were annealed at 100 °C for two hours under nitrogen. The thickness of the P3HT and PMMA layers were about 30 nm and 300 nm, respectively, as measured by atomic force microscope (AFM). Finally, gold source/drain electrodes were evaporated through a shadow mask, defining 60 µm long and 2 mm wide OFET channels. All the devices were stored in ambient air in the dark. The air stability was determined by measuring the device current-voltage (I–V) characteristics over an extended time period with a semiconductor parameter analyzer (Agilent E5270B). On each kind of the PMMA layer, we fabricated nine P3HT OFET devices. All of the nine devices were measured every time. Similar OFET results could be achieved on the same kind of PMMA layer. For comparison, we selected the best results of OFETs on each kind of PMMA layer.

3. Results and discussion

Typical OFET transfer characteristics were measured in the saturation regime with a source-drain voltage of $V_{SD} = -80$ V. Fig. 1 shows the I–V results of P3HT OFETs on the PMMA dielectric layers under different process conditions. The time dependence of the device performance presents different trends. For clarity, the transfer characteristics measured after 4 h were not included in Fig. 1 due to no difference observed from those of the as-prepared devices. When compared to our previous results with air stability of about 120 min on the n-octadecyltrichlorosilane (OTS) treated SiO2 dielectric layer in Ref. [16], vast improvement in device stability was observed on OFETs prepared on the PMMA dielectric layers, with only a slight degradation of OFETs when subjected to ambient air for up to a few days. A summary of the performance of the as-prepared P3HT OTF is given in Table 1, including field-effect mobility, threshold voltage, on/off current ratio, and subthreshold slope. For each gate interface structure, we also measured the total gate dielectric capacitance using an Agilent E4980 LCR meter and the values (at 10⁶ Hz) are shown in Table 1 as well.

Fig. 1. Ambient-air stability of P3HT-based OFETs with the PMMA gate dielectric layers cured at (a) 120 °C, (b) 150 °C, (c) 180 °C, and by (d) UV treatment.
Table 1

Field-effect mobility, threshold voltage, on/off ratio, and subthreshold slope of as-prepared P3HT OFETs deposited on PMMA dielectric layers with different process conditions.

<table>
<thead>
<tr>
<th>PMMA process condition</th>
<th>Capacitance (nF/cm²)</th>
<th>μ (cm²/V s⁻¹ S⁻¹)</th>
<th>V_T (V)</th>
<th>I_on/I_off</th>
<th>S (V/decade)</th>
</tr>
</thead>
<tbody>
<tr>
<td>120 °C annealed</td>
<td>41.8</td>
<td>0.0087</td>
<td>−3.1</td>
<td>1.6 × 10⁵</td>
<td>6.8</td>
</tr>
<tr>
<td>150 °C annealed</td>
<td>43.6</td>
<td>0.0045</td>
<td>6.0</td>
<td>1.0 × 10⁵</td>
<td>3.2</td>
</tr>
<tr>
<td>180 °C annealed</td>
<td>44.7</td>
<td>0.0045</td>
<td>12.9</td>
<td>6.8 × 10⁴</td>
<td>4.2</td>
</tr>
<tr>
<td>UV treated</td>
<td>48.2</td>
<td>0.0074</td>
<td>−3.7</td>
<td>2.5 × 10⁵</td>
<td>2.3</td>
</tr>
</tbody>
</table>

*a The on/off ratio for all samples was calculated at the gate voltages of −100 V and 40 V.

As shown in Fig. 1(a), the OFETs with the PMMA layer prepared at 120 °C had a lower threshold voltage V_T = −3.1 V, but a larger (inverse) subthreshold slope S = 6.8 V/decade, than the OFETs prepared under the other conditions. V_T is usually determined by the doping level in the active channel while S is associated with the carrier trap density at the interface between the active layer and the dielectric layer. The mobility in the saturation regime was determined as 0.0087 cm²/V s [5]. The mobility value was higher than or comparable to the values reported for such kind of P3HT in ambient air [20]. From the transfer curves measured after different storage time, the device was doped in days, which still demonstrated better air stability than those on OTS-treated substrates as in our previous report [16].

The stability was found to be dramatically improved in OFETs with the PMMA dielectric layer prepared at a higher temperature like 150 °C as shown in Fig. 1(b). Little change in the transfer characteristics was observed even after 3 days. The mobility, μ = 0.0045 cm²/V s, was a little lower than that of the OFETs with the PMMA layer cured at 120 °C but typical for devices produced by this kind of P3HT in ambient air [20]. It is interesting to observe that the threshold voltage, V_T = 6 V, increased while the subthreshold slope, S = 3.2 V/decade, decreased with the PMMA curing temperature. The increased V_T suggests a relatively high doping level in the as-prepared devices as they were transferred from the vacuum chamber to the measurement setup, which normally took about 10 min. The improved S indicates that there were much fewer carrier traps at the interface between the PMMA and P3HT active layers [21]. The results thus might suggest that charge trap density at the interface plays an important role in the OFET air stability.

For the devices on the PMMA layer cured at an even higher temperature of 180 °C, as shown in Fig. 1(c), the threshold voltage dramatically increased to 12.9 V while the subthreshold slope slightly increased to 4.2 V/decade. Much more doping might be expected in the P3HT layer, which can be evidenced by AFM measurements later. The off current apparently increased when the devices were stored for more than 1 day, indicating an inferior stability in comparison to the results of OFETs with the PMMA layer prepared at 150 °C. Given the above results, we might think that 150 °C is the optimal PMMA-curing temperature to improve
the stability of P3HT OFETs with the reasonable mobility of 0.0045 cm²/Vs and high on/off current ratio of 10³.

We further examine the P3HT-based OFETs on the PMMA dielectric layer which was treated by UV exposure instead of thermal treatment. As shown in Fig. 1(d), the air stability was comparable to the devices on the PMMA layer annealed at 150 °C. Most importantly, the mobility could reach 0.0074 cm²/Vs, and on/off current ratio was well above 10³. The device switched on around 0 V (Vgs = -3.7 V) and S was nicely reduced to 2.3 V/decade. A relatively high and stable OFET performance has been demonstrated for P3HT on PMMA films cured by UV exposure, implying the polymeric dielectric layer could be optimized to realize full organic electronic devices.

To gain an insight into the stability differences, the P3HT films were imaged using an atomic force microscope in the tapping mode. PMMA layers under different process conditions have also been imaged before coating the P3HT layer. Very smooth surface with the area fluctuation RMS less than 0.6 nm was observed for all the PMMA layers cured under different conditions. Such differences indicate that the difference at the interface between the P3HT and PMMA layers might come from the interaction of the two layers rather than from the morphology and performance of the PMMA layer only. As shown in Fig. 2(a)–(c), the area fluctuation RMS and the maximum peak-to-valley fluctuation Zmax increased with increasing temperatures. The P3HT film on the PMMA layer cured at 120 °C in Fig. 2(a) appeared relatively more amorphous and uniform, which suggests good connectivity among the P3HT polymers and hence a high field-effect mobility [12]. The device stability, however, was inferior. This is understandable because the glass transition temperature (Tg) of PMMA is 122 °C and the curing temperature of 120 °C is not sufficient to densely pack the P3HT film. During the P3HT film spin-coating process, the PMMA layer was dissolved by TCB solvent, which could be observed as the swelling on the P3HT surface by an optical microscope (not shown here). This thus led to a less crystalline or an amorphous film, as shown in Fig. 2(a), and a large carrier trap density at the interface between the active channel and the insufficiently cured PMMA layer, which is associated with a relatively high subthreshold voltage S = 6.8 V/decade. And the large trap density at the interface causes the quick deterioration of the OFETs when subjected to ambient air.

The thermal treatment of the polymeric layer is generally required to conduct at a temperature higher than Tg by 20–30 °C for reducing the free volume formed inside the layer during spin coating and facilitating the polymer chains to move freely and to be effectively cross-linked [22]. Hence, a compact PMMA layer could be achieved as raised the curing temperatures to 150 °C, and no swelling and dissolution by TCB over the time scale of the experiment was observed. Fig. 2(b) shows apparent boundaries of the formed P3HT crystalline domains, corresponding to the increased roughness RMS of 1.95 nm and larger Zmax of 13.1 nm. This caused a lower mobility than that of the OFETs with the PMMA layer cured at 120 °C due to the increasing charge carrier scattering [23]. Because of the tightly packed P3HT molecules, it was very difficult for oxygen and water molecules to further penetrate through the domains, thereby resulting in a more sustained stability as shown in Fig. 1(b). When the PMMA buffer layer was cured at an even higher temperature of 180 °C, rather rough surface (Zmax is 14.8 nm) was observed in Fig. 2(c). A few deep pinholes were formed in the P3HT layer and might facilitate oxygen and water molecules penetrating the film and doping the P3HT at the semiconductor–insulator interface, where the hole transport took place. This resulted in a larger VT of 12.9 V and a low stability as compared to the devices with the PMMA layer cured at 150 °C. The observation demonstrates the importance of interfacial interactions on both the microstructure of the active layers and the performance of P3HT OFETs, which is in agreement with the recent report by Sethuraman et al. [12].

The results above show that both the top active layer quality and the carrier trap density at the interface play a key role in the device degradation mechanism [1,20,21]. To achieve high device mobility and stable performance, two important aspects should be concerned. Firstly, the connectivity of the P3HT molecules and the uniformity of the active layer not only play an important role in the field-effect mobility, but also affect the vulnerability of the active layer to the penetration of the oxygen and moisture in air. Secondly, in the bottom-gate top-contact OFET structure, the nanocrystalline islands in the P3HT films nucleate from the dielectric interface. The reliable device performance requires a compact and less defective polymeric buffer layer, that is, a low charge-trap density at the interface.

The P3HT film on the PMMA layer cured by UV exposure satisfies these two considerations and has an excellent combination of high carrier mobility and enhanced air stability. As shown in Fig. 2(d), its RMS was 0.32 nm and Zmax was 2.57 nm, which were far smaller than those of the surfaces in the other three micrographs. The better film uniformity not only led to a mobility increase, but also dramatically enhanced the air stability. The low carrier-trap density at the interface, indicated by the small S value as shown in Table 1, was another reason for the improved stability. It has to be mentioned that because of the lack of the sufficiently thermal treatment, the dissolution by TCB also occurred in the UV-cured PMMA layer. Similar swelling in the P3HT layer could be observed by optical microscope as that with the PMMA layer cured at 120 °C. However, the low subthreshold voltage of 2.3 V/decade indicates that the carrier trap density at the interface between the active channel and the UV-cured PMMA layer is much lower than that at the inter-

![Fig. 3. The plot of (a) Ion/Ioff current ratio and (b) threshold voltage as a function of exposure time in an ambient condition. The fitted dash lines in (b) show that VT proportionally increases as time passes.](image-url)
face between the P3HT layer and the PMMA layer cured at 120 °C. The reason is not clear but may be related to the effects of the UV exposure on the PMMA surface. New method like high-resolution transmission electron microscopy could provide more information on the interaction of the PMMA and P3HT layers, which might give useful insight into the physical understanding of the effect on the device performance.

Fig. 3 shows the $I_{on}/I_{off}$ current ratio and threshold voltage as a function of exposure time to an ambient condition in an even longer time scale. All the P3HT OFET devices were stored in ambient air in the dark, and measured over an extended time period. When the $I_{on}/I_{off}$ current ratio decreases over two orders of magnitude, we stopped the measurements. OFETs on the UV-cured PMMA layer had rather stable $I_{on}/I_{off}$ current ratio over $10^5$ for more than 10 days while those on the PMMA layers cured at 120 and 180 °C deteriorated only after 2 days as shown in Fig. 3(a). The devices on PMMA cured at 150 °C showed the moderate stability with respect to the variation of the $I_{on}/I_{off}$ current ratio. The time dependence of the threshold voltage, shown in Fig. 3(b), presents the similar trends. The slope of the fitted line in Fig. 3(b) indicates the $V_T$ variation rate with time. In contrast to the relatively fast increase of $V_T$ for OFETs with the thermal annealed PMMA layers, the devices with the UV-treated PMMA layer showed a stable $V_T$ with little change for about 10 days.

4. Conclusions

In summary, enhanced stability has been demonstrated in the P3HT-based OFETs with optimally cured PMMA dielectric layers. In the bottom-gate top-contact OFET structure, both the quality in the top active layer and the charge trap density at the interface influence the device air stability. Comparing the experimental results of the OFETs with the PMMA layers cured by thermal treatment and UV exposure, the P3HT film on the PMMA layer cured by the latter method possessed good film uniformity in the active layer and less carrier trap density at the interface. This thus accounted for the high and stable OFET performance with high on/off current ratio over $10^5$ and relatively stable $V_T$ even after the storage time of 10 days. To achieve the full organic electronic devices, further investigation should be carried out, such as examining the OFET performance with only one layer of polymeric dielectric layer, clarifying the influence of UV treatment on the interface states, etc.

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